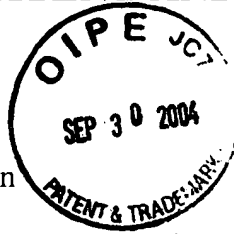


IN THE UNITED STATES PATENT AND TRADEMARK OFFICE



In re Application of :

Sandeep Bhutani
Subramanian Venkateswaran

Serial No. :

09/515,376

Group Art Unit :

2123

Filed :

February 29, 2000

Examiner :

Phan, Thai

For :

4 Point Derating Scheme for
Propagation Delay And Setup/Hold
Time Computation

Atty Docket :

30454-00243 / C4-4247

I hereby certify that this correspondence is being deposited with the U.S. Postal Service as First Class Mail in an envelope addressed to: Commissioner for Patents, P. O. Box 1450, Alexandria, VA 22313-1450, on the date below:

Manu Kashyap

9/30/04
Date

Signature

SUBMISSION OF FORMAL DRAWINGS PURSUANT TO 37 C.F.R. §1.85

Official Draftsman

Commissioner for Patents
P. O. Box 1450
Alexandria, VA 22313-1450

Sir:

Applicant hereby substitutes the enclosed formal drawings for those presently in the above referenced application.

LSI Logic Corporation
1621 Barber Lane, MS D-106
Milipitas, CA 95035
408-433-7475

Respectfully submitted,

Sandeep Jaggi

Reg. No. 43,331

Date: 9/30/04